

0FFA8

FFA8 D8 CLD
FFA9 A2D4 CBX #D4
FFAB A9D0 LDA #D0
FFAD 8501 STA \$01
FFAF A000 LDY #00
FFB1 8400 STY \$00
FFB3 A920 LDA #20
FFB5 9100 STA (\$00),Y
FFB7 C8 INY
FFB8 D0FB BNE \$FFB5
FFBA EE01 INC \$01
FFBC E401 CPX \$01
FFBE D0F5 BNE \$FFB5
FFC0 A903 LDA #03
FFC2 8D00FC STA \$FC00
FFC5 A9B1 LDA #B1
FFC7 8D00FC STA \$FC00
FFCA B9A4FF LDA \$FFA4,Y
FFCD 300E BMI \$FFDD
FFCF 99C6D0 STA \$D0C6,Y
FFD2 AE01FE LDX \$FE01
FFD5 D003 BNE \$FFDA
FFD7 200BFE JSR \$FE0B

FFDA C8 INY
FFDB D0ED BNE \$FFCA
FFDD AD01FE LDA \$FE01
FFDE D005 BNE \$FFE7
FFE2 2000FE JSR \$FE00
FFE5 B003 BCS \$FFEA
FFE7 20EDFE JSR \$FEED
FFEA C944 CMP #44
FFEC D009 BNE \$FFF7
FEE 2000FF JSR \$FF00
FFF1 4C0022 JMP \$2200
FFF4 2000FF JSR \$FF00
FFF7 6CFCFE JMP (\$FEFC)
FFFA 3001 BMI \$FFFD
FFFC A8 TAY
FFFD FF ???
FFE C001 CPY #01
0000 00 BRK
0001 2A ROL A
0002 00 BRK
0003 40 RTI
0004 E103 SBC (\$03,X)
0005 B3 ???

500' board
ROM

FF00 A000 LDY #000
 FF02 8C01C0 STY \$C001
 FF05 8C00C0 STY \$C000
 FF08 A204 LDX #04
 FF0A 8E01C0 STX \$C001
 FF0D 8C03C0 STY \$C003
 FF10 89 DEY
 FF11 8C02C0 STY \$C002
 FF14 8E03C0 STX \$C003
 FF17 8C02C0 STY \$C002
 FF1A A9FB LDA #\$FB
 FF1C 2089FF JSR \$FF89
 FF1F A218 LDX #18
 FF21 2074FF JSR \$FF74
 FF24 A902 LDA #\$02
 FF26 2C00C0 BIT \$C000
 FF29 F008 BEQ \$FF33
 FF2B A9FF LDA #\$FF
 FF2D 2089FF JSR \$FF89
 FF30 4C24FF JMP \$FF24
 FF33 A27F LDX #7F
 FF35 8E02C0 STX \$C002
 FF38 2074FF JSR \$FF74

FF3B AD00C0 LDA \$C000
 FF3E 30FB BMI \$FF3B
 FF40 AD00C0 LDA \$C000
 FF43 10FB BPL \$FF40
 FF45 A903 LDA #\$03
 FF47 8D10C0 STA \$C010
 FF4A A958 LDA #\$58
 FF4C 8D10C0 STA \$C010
 FF4F 207FFF JSR \$FF7F
 FF52 8501 STA \$01
 FF54 207FFF JSR \$FF7F
 FF57 8500 STA \$00
 FF59 207FFF JSR \$FF7F
 FF5C 8502 STA \$02
 FF5E A000 LDY #000
 FF60 207FFF JSR \$FF7F
 FF63 9100 STA (\$00),Y
 FF65 C8 INY
 FF66 D0F8 BNE \$FF60
 FF68 E601 INC \$01
 FF6A C602 DEC \$02
 FF6C D0F2 BNE \$FF60
 FF6E A9FF LDA #\$FF

LD 500

to get.

ADD 2500

LDA 1100

C 10 10M
BNE