

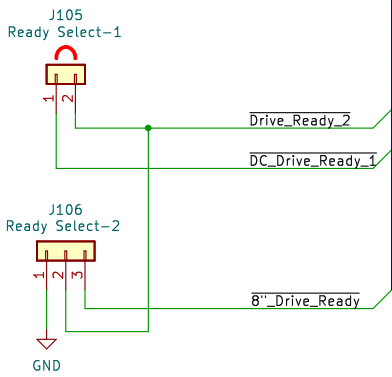
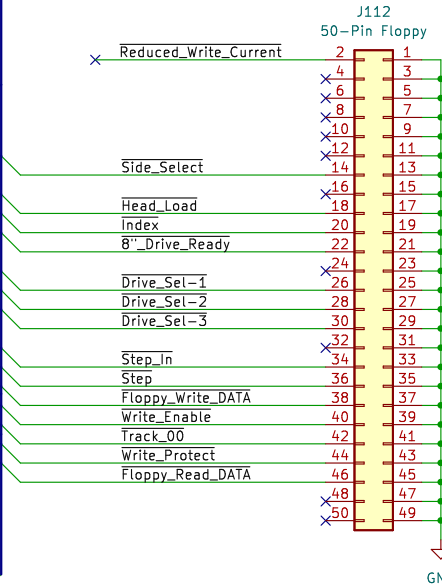
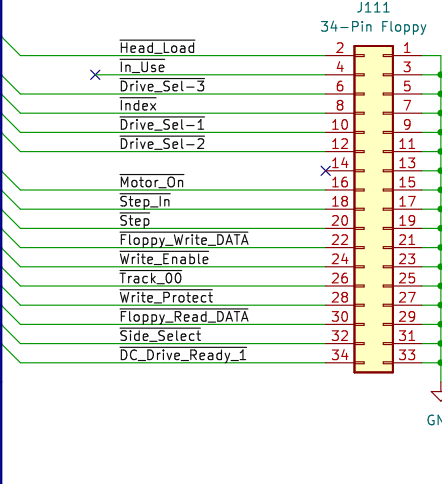
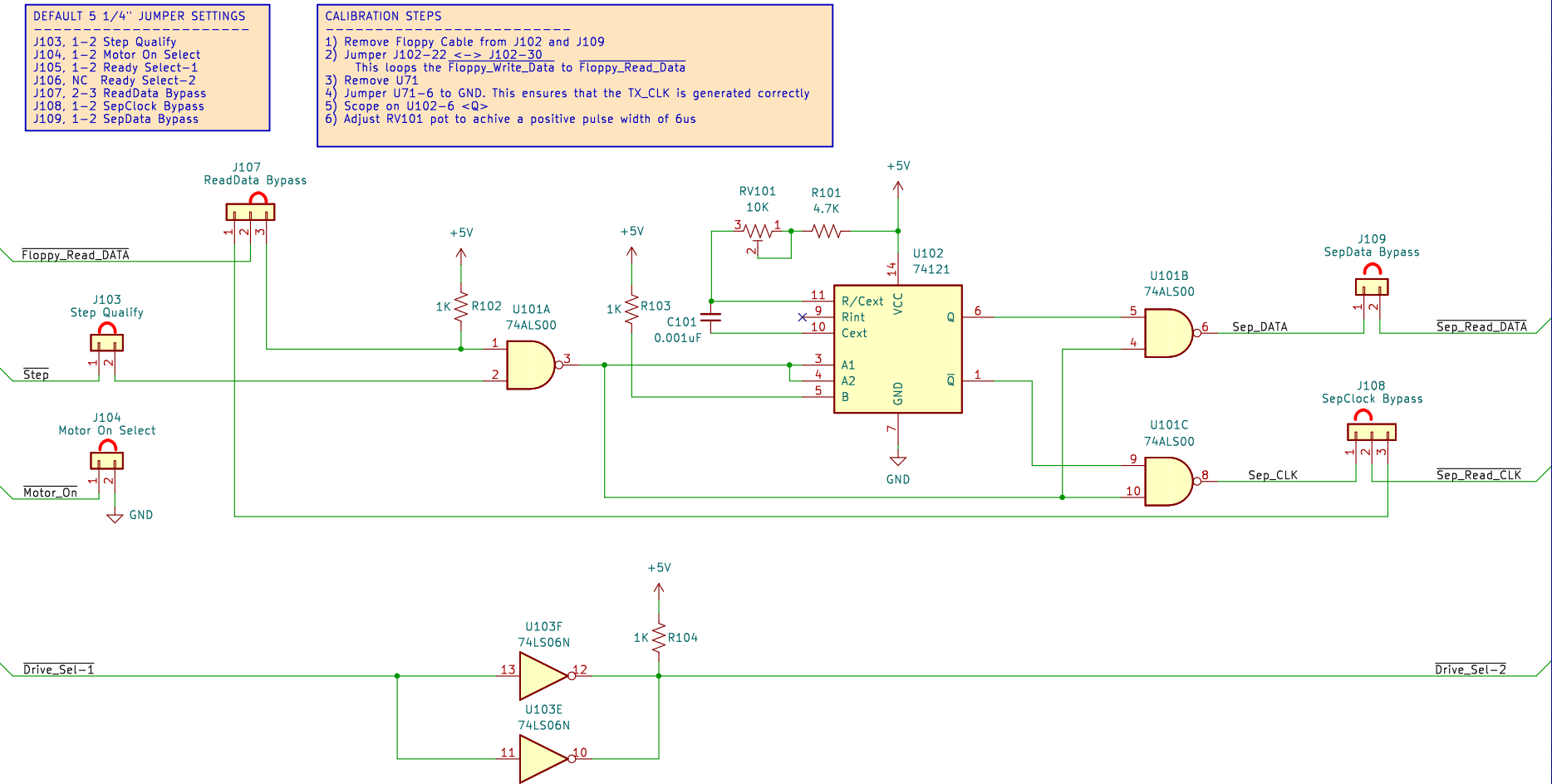
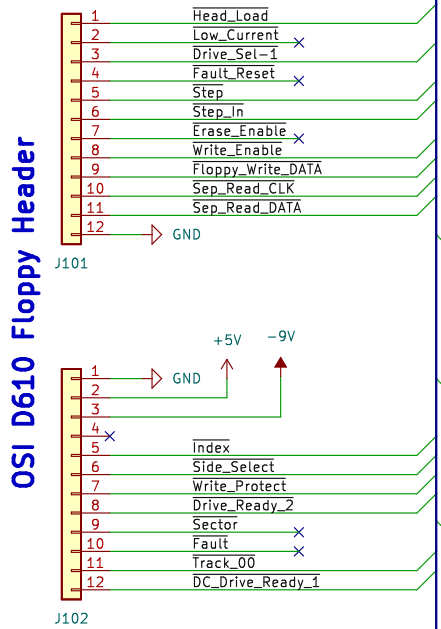
{Seperator\_Signals}

DEFAULT 5 1/4" JUMPER SETTINGS

J103, 1-2 Step Qualify  
J104, 1-2 Motor On Select  
J105, 1-2 Ready Select-1  
J106, NC Ready Select-2  
J107, 2-3 ReadData Bypass  
J108, 1-2 SepClock Bypass  
J109, 1-2 SepData Bypass

CALIBRATION STEPS

1) Remove Floppy Cable from J102 and J109  
2) Jumper J102-22 <=> J102-30  
This loops the Floppy\_Write\_Data to Floppy\_Read\_Data  
3) Remove U71  
4) Jumper U71-6 to GND. This ensures that the TX\_CLK is generated correctly  
5) Scope on U102-6 <Q>  
6) Adjust RV101 pot to achive a positive pulse width of 6us



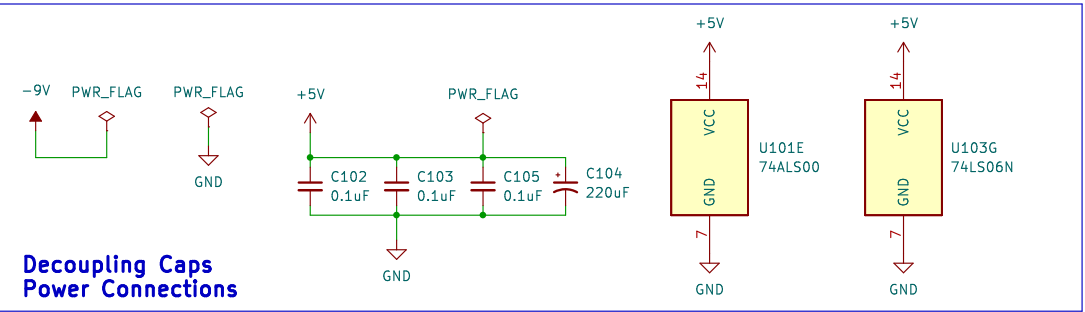
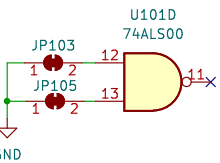
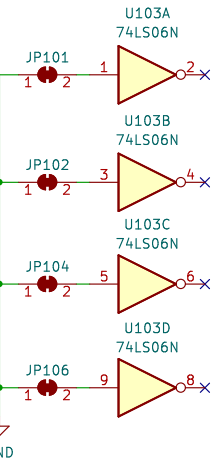
DESC101  
OSI Data Seperator

DESC102  
Rev 1.2

H101  
MountingHole

JLCJLCJLCJLC  
DESC103

Fiducials, Mounting Holes, etc.



Drawn By: A.Kissel  
Reviewed By:  
**Kissel Enterprises**  
Sheet: /  
File: OSI\_Floppy\_Data\_Seperator.kicad\_sch

**Title:**

Size: B	Date: 2023-11-13	Rev: REV 1.3
KiCad E.D.A.	kicad 7.0.9	Id: 1/1